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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/791,816	03/04/2004	Min-Hung Lec	0941-0924P 3667	
2292	7590 10/11/2005		EXAMINER	
BIRCH STE PO BOX 747	WART KOLASCH &	HU, SHOUXIANG		
FALLS CHURCH, VA 22040-0747			ART UNIT	PAPER NUMBER
	,		2811	

DATE MAILED: 10/11/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

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	Application No.	Applicant(s)					
	10/791,816	LEE ET AL.					
Office Action Summary	Examiner	Art Unit					
	Shouxiang Hu	2811					
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply							
A SHORTENED STATUTORY PERIOD FOR REPLY WHICHEVER IS LONGER, FROM THE MAILING DA  - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication.  - If NO period for reply is specified above, the maximum statutory period w  - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUNICATION  36(a). In no event, however, may a reply be tim  will apply and will expire SIX (6) MONTHS from  cause the application to become ABANDONEI	l. ely filed the mailing date of this c O (35 U.S.C.§ 133).					
Status							
<ul> <li>1) Responsive to communication(s) filed on 28 July 2005.</li> <li>2a) This action is FINAL.</li> <li>2b) This action is non-final.</li> <li>3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.</li> </ul>							
Disposition of Claims			•				
4) ☐ Claim(s) 1-16 is/are pending in the application. 4a) Of the above claim(s) 9-16 is/are withdrawr  5) ☐ Claim(s) is/are allowed.  6) ☐ Claim(s) /- 8 is/are rejected.  7) ☐ Claim(s) is/are objected to.  8) ☐ Claim(s) are subject to restriction and/or	from consideration.						
Application Papers							
9)☐ The specification is objected to by the Examine							
10)☐ The drawing(s) filed on is/are: a)☐ accepted or b)☐ objected to by the Examiner.							
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).							
11) The oath or declaration is objected to by the Ex							
Priority under 35 U.S.C. § 119							
12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of:  1. Certified copies of the priority document 2. Certified copies of the priority document 3. Copies of the certified copies of the priority document application from the International Bureau * See the attached detailed Office action for a list	s have been received. s have been received in Applicati rity documents have been receive u (PCT Rule 17.2(a)).	on No ed in this National	Stage				
Attachment(c)			·				
Attachment(s)  1) Notice of References Cited (PTO-892)  4) Interview Summary (PTO-413)							
2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date	Paper No(s)/Mail Da		O-152)				

### **DETAILED ACTION**

### Claim Rejections - 35 USC § 112

- 1. The following is a quotation of the first paragraph of 35 U.S.C. 112:
  - The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.
- 2. Claims 6 and 7 are rejected under 35 U.S.C. 112, first paragraph, as failing to comply with the written description requirement. The claim(s) contains subject matter which was not described in the specification in such a way as to reasonably convey to one skilled in the relevant art that the inventor(s), at the time the application was filed, had possession of the claimed invention. Claims 6 and 7 respectively recite the subject matters that the gate electrode comprises both polysilicon and poly-SiGe, and that the metal gate recited in claim 5 comprises dopants; but they each lack sufficient support from the original disclosure.

## Claim Rejections - 35 USC § 103

- 3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
  - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

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Claims 1-8, insofar as being in compliance with 35 U.S.C. 112, are rejected under 35 U.S.C. 103(a) as being unpatentable over Fitzgerald (US 2002/0125471) in view of Puchner (US 6,358,806).

Fitzgerald discloses a strained-channel MOSFET structure (Fig. 5A), comprising: a substrate; a graded SiGe layer (502) on the substrate; a relaxed buffer layer (506; SiGe) on the graded SiGe layer; a strained channel-forming layer (508); a gate dielectric layer (510); a polysilicon gate electrode (512); and source and drain regions (513, 514).

Although Fitzgerald does not expressly disclose that the strained channelforming layer can also be SiC, one of ordinary skill in the art would readily recognize
that SiC is also an art-known commonly used semiconductor material and possesses
desirable performance at high temperature and/or at high power, and that strained SiC
is desirable for forming a channel layer with improved channel performance, as
evidenced in the prior art such as Puchner (see col. 2, lines 8-11).

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to incorporate the strained SiC channel-forming layer of Puchner into the device of Fitzgerald, so that a MOSFET with desirable and/or improved channel performance would be obtained.

Regarding claims 3 and 5-7, it is art known that a gate dielectric layer can be desirably formed of a high dielectric constant material; and that metal, doped polysilicon and doped poly-SiGe are all well-known conductive materials commonly used for forming the gate electrode, as readily evidenced in the prior art such as Currie et al. (US 2003/0057416; see Paragraphs 0045 and 0046).

Regarding claim 8, it is noted that it is art-known that the semiconductor substrate can be formed of each of the recited doped Ge, III-V group semiconductor, or silicon-on-insulator (SOI).

### Response to Arguments

4. Applicant's arguments filed on July 28, 2005 have been fully considered but they are not persuasive.

Applicant's main arguments include: the cited references do not teach the claimed invention.

In response to applicant's argument that the references fail to show certain features of applicant's invention, it is noted that the features upon which applicant relies (i.e., does not require a silicon capping layer on the strained silicon carbon alloy layer) are not recited in the rejected claim(s). Although the claims are interpreted in light of the specification, limitations from the specification are not read into the claims. See *In re Van Geuns*, 988 F.2d 1181, 26 USPQ2d 1057 (Fed. Cir. 1993).

Both Fitzgerald and Puchner discloses a gate dielectric layer on the stained Si or SiC channel layer, as the term of "on" therein can be interpreted as meaning: above, covering, or in proximity to; and it does not have to be interpreted as meaning "in contact with". Thus, the combined teachings of Fitzgerald and Puchner do resulted in the invention as defined in the above rejected claims.

#### Conclusion

5. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Shouxiang Hu whose telephone number is 571-272-1654. The examiner can normally be reached on Monday through Thursday, 7:30 AM to 6:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Eddie C. Lee can be reached on 571-272-1732. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

SH

October 4, 2005

SHOUXIANG HU PRIMARY EXAMINER